

Amendments to the Claims

1. (Currently Amended) A digital video-processing unit for processing an input signal to an output data signal for a display unit, said video-processing unit comprising

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- ~~a processing means~~ processor for processing the input signal to the output data signal,
 - memory means for storing the input signal prior to supply to said ~~processing means~~ processor, and
 - a memory manager coupled with the ~~processing means~~ processor and the memory means, the memory manager being arranged to transfer the input data signals to the memory means and to transfer the stored input data signal from the memory means to the ~~processing means~~ processor, respectively, ~~characterized in that~~ wherein an output of the ~~processing means~~ processor is coupled to an input of the memory manager, ~~and in that~~ wherein the memory manager is further arranged to transfer the output data signal from the ~~processing means~~ processor to the memory means for storing the output data signal, and to transfer the stored output data signal from the memory means to the display unit, and wherein the memory manager is further arranged to transfer the stored data output signal from the memory means to the processor, and said processor is further arranged to execute a further operation on the stored data output signal.
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2. (Cancelled).

3. (Currently Amended) The digital video-processing unit of claim 1, wherein said ~~processing~~
~~means~~ processor and said memory manager are arranged to execute different processes in time
multiplex.

4. (Currently Amended) The digital video-processing unit of claim 1, wherein said ~~processing~~
~~means~~ processor, said memory manager and the memory means are comprised on a single
integrated circuit.

5. (Currently Amended) The digital video-processing unit of claim 1, wherein said ~~processing~~
~~means~~ processor is arranged to resize an image represented by said input data signal.

6. (Currently Amended) The digital video-processing unit of claim 1, wherein said ~~processing~~
~~means~~ processor is further arranged to convert said input data signal representing an image into a
color-sequential output signal.

7. (Currently Amended) The digital video-processing unit of claim 1, wherein said ~~processing~~
~~means~~ processor is arranged to convert said input data signal representing an image into a sub-
field modulated output signal for controlling the display unit.

8. (Original) The digital video-processing unit of claim 1, wherein a part of the memory means is
arranged as a cyclic memory for storing a part of an image represented by the input data signal.

9. (Original) The digital video-processing unit of claim 1, wherein the memory manager has a further input for a second data input signal, and the memory manager is arranged to transfer the second data input signal to the memory means.

10. (Original) A video display system comprising a digital video-processing unit as claimed in claim 1, having an output for providing the output data signal and a display system coupled to the output of said digital video-processing unit for displaying of said output data signal.